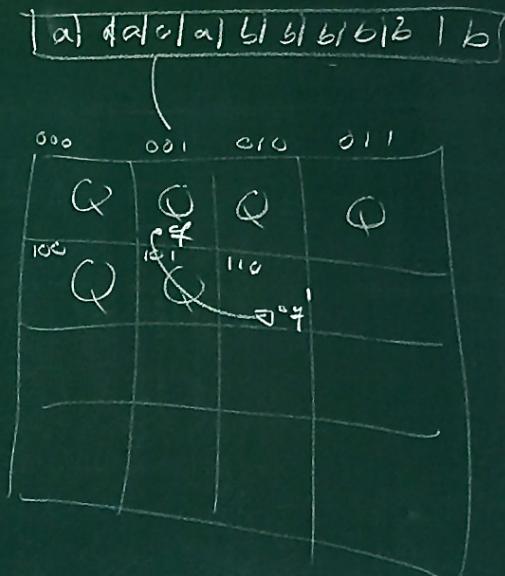
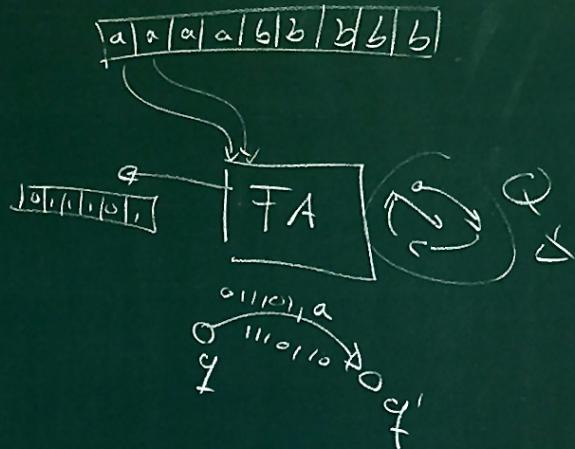


2. Memory, Tapes and TMs

Fin. t.c Automata

Memory?

- Registers, variable $\{0,1\}^C$



Storage of
finite memory
in the state space

2. Memory, Tapes and TMs

Finit. Automata

Memory?

- Registers, variable $\{0,1\}^*$
- Stack



- FA + stack $\subseteq \text{CFL}$ (context-free languages)

det. Pushdown-Automata

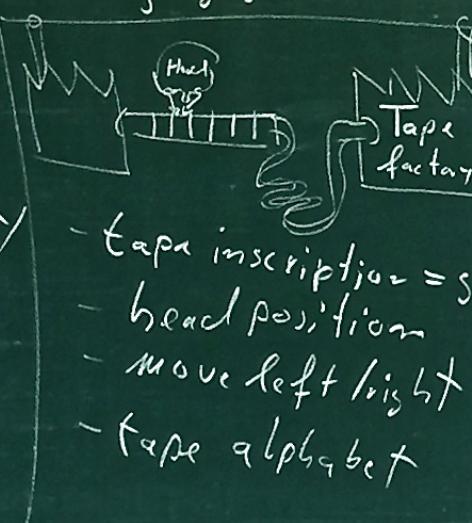
$$L = \{a^n b^n c^n \mid n \in \mathbb{N}\}$$

$$\{\omega \omega \mid \omega \in \Sigma^*\} = \text{COPY}$$

- FA + 2 stacks

- Infinit. tape

2. Memory, Tapes and TMs

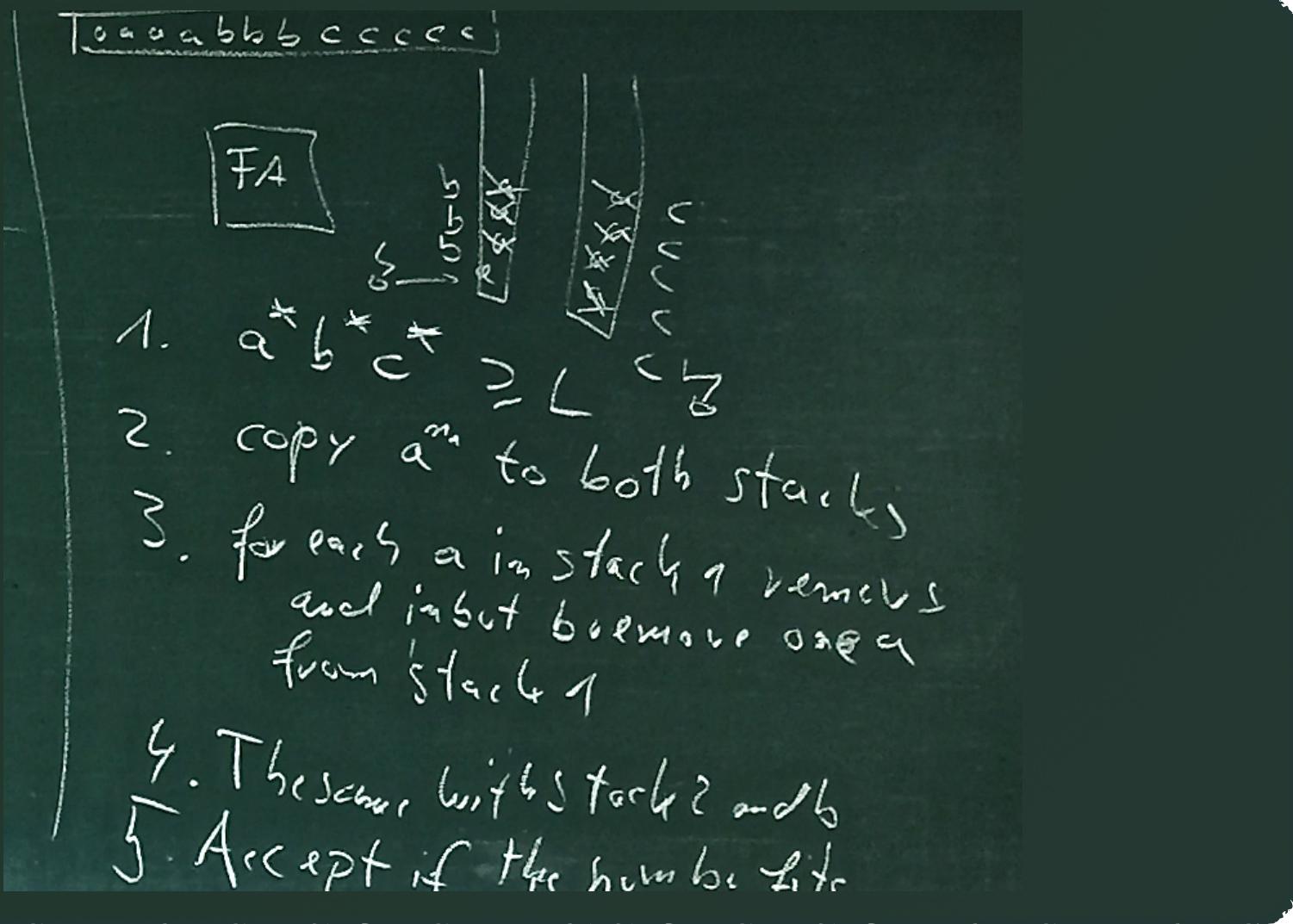
- FA + stack $\subseteq \text{CFL}$ (context-free languages)
det. Pushdown-Automata
 - $L = \{a^n b^n c^n \mid n \in \mathbb{N}\}$
 - $\{\omega \omega \mid \omega \in \Sigma^*\} = \text{COPY}$
 - FA + 2 stacks
 - infinite tape
- 

The diagram shows a horizontal tape with a head at the top. The tape is labeled "Tape" and "factory". A wavy line above the tape is labeled "Head". Below the tape, there are two arrows pointing left and right, labeled "move left/right".

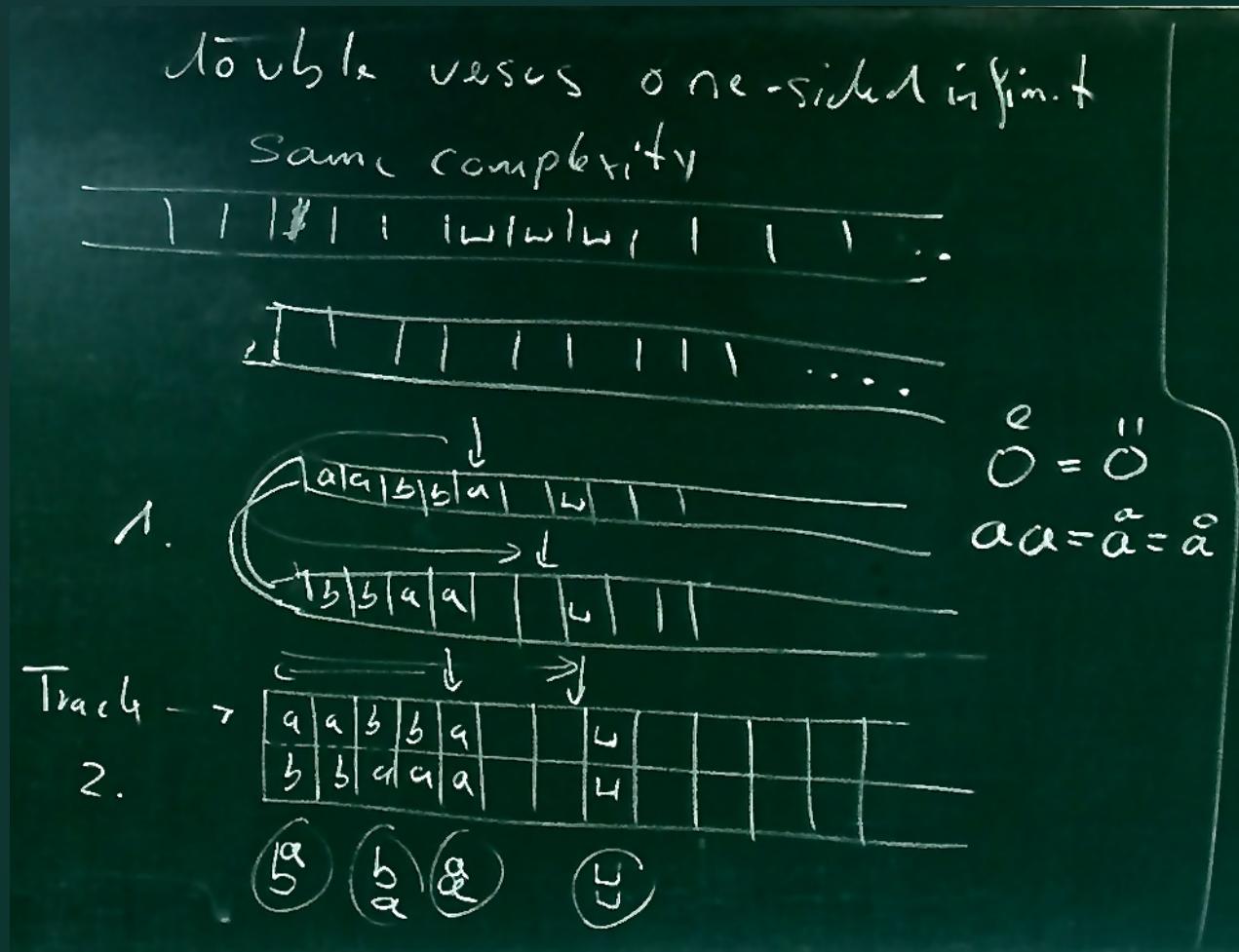
 - tape inscription = string $\in \Sigma^*$
 - head position
 - move left/right
 - tape alphabet

$\boxed{FA_1} \quad | \quad \boxed{FA_2}$

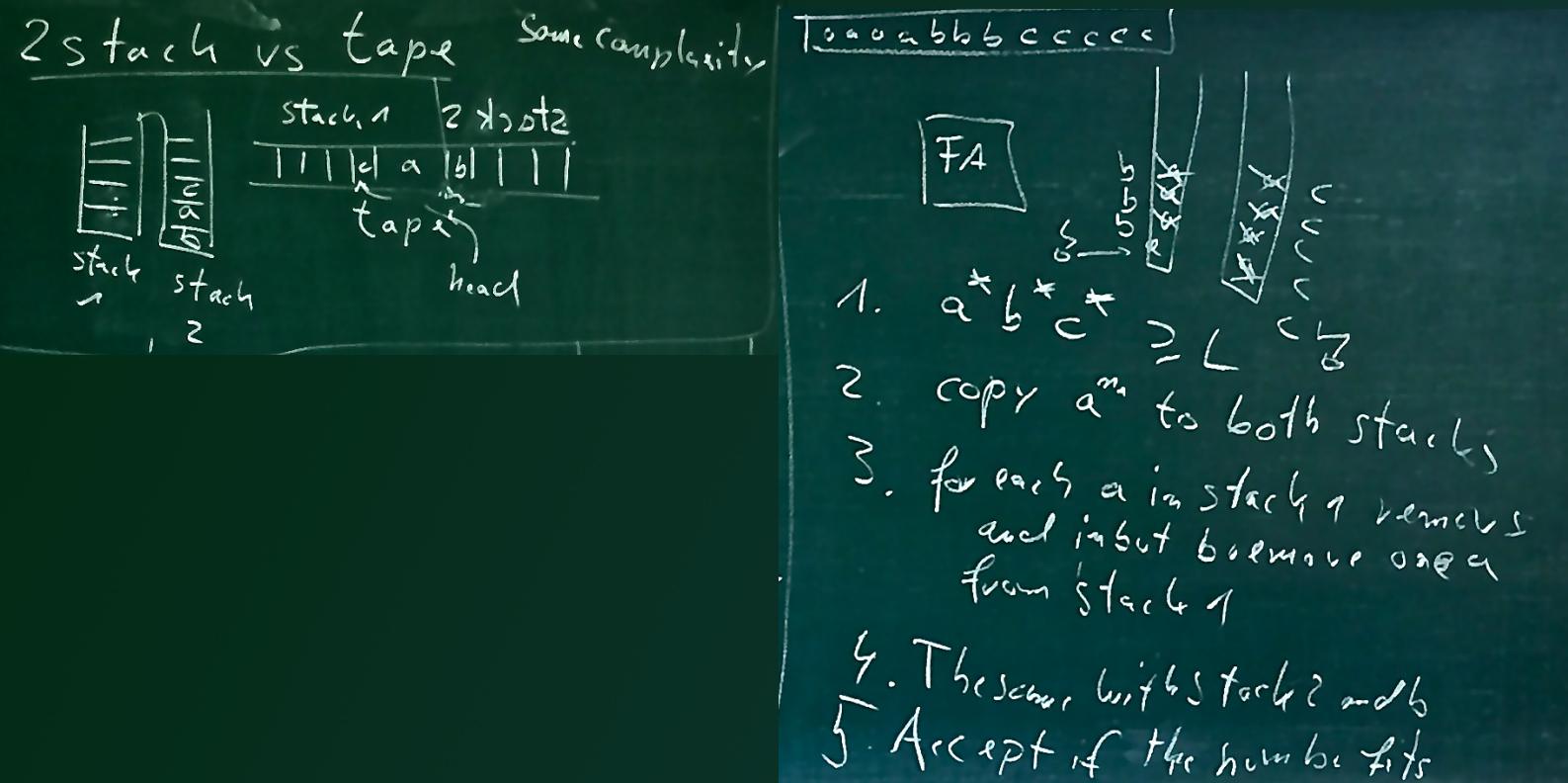
2. Memory, Tapes and TMs



2. Memory, Tapes and TMs

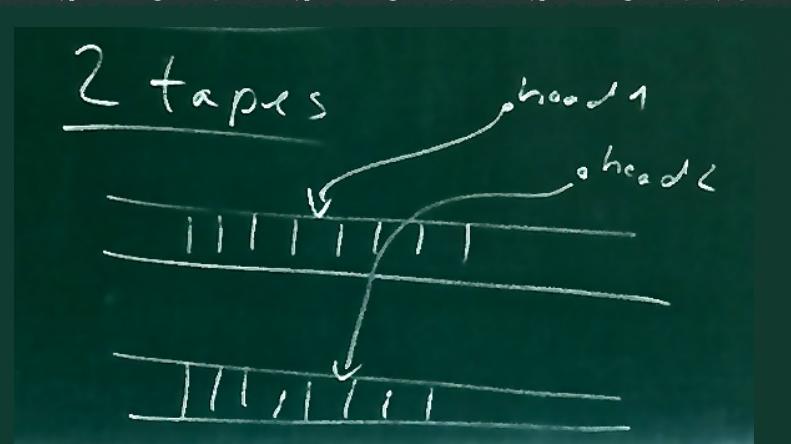


2. Memory, Tapes and TMs



2. Memory, Tapes and TMs

Lemma Every TM with k tapes with time t and space S can be simulated by a 1 tape TM in time $2 \cdot k \cdot S \cdot t$ and spaces.



Proof

1. One tape with k tracks (copies of the k tapes) + special marks for the head (extra k tapes)

2. Memory, Tapes and TMs



Simulation

time $2 \cdot s \cdot k$

$\left\{ \begin{array}{l} \text{Read for each tape} \\ - \text{go to head} \\ - \text{store letter in fin. memory} \end{array} \right.$

compute

time $2 \cdot s \cdot h$

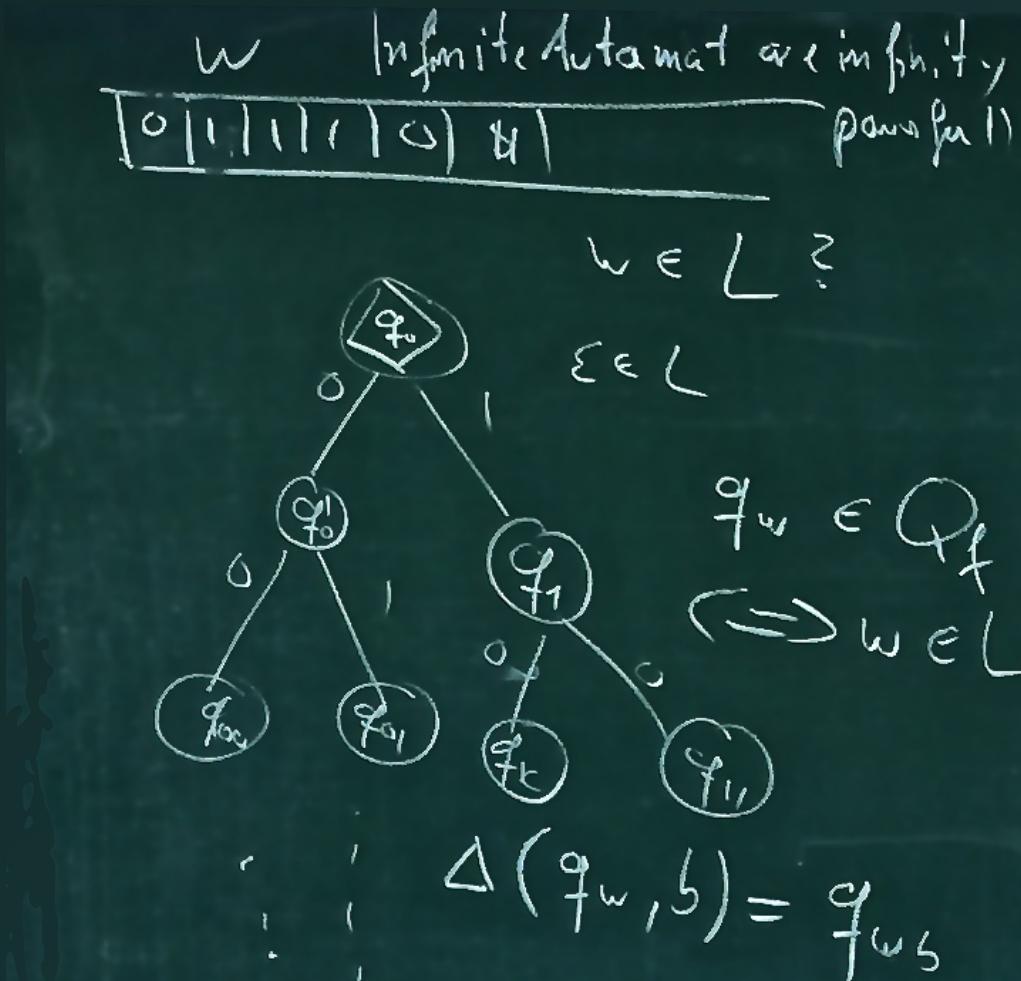
$\left\{ \begin{array}{l} \text{for fin} \\ \text{Wait & Move for each tape (track)} \\ - \text{go to head} \end{array} \right.$

Repeat until finish

overall time: $2 \cdot h \cdot s \cdot k$

□

2. Memory, Tapes and TMs



2. Memory, Tapes and TMs

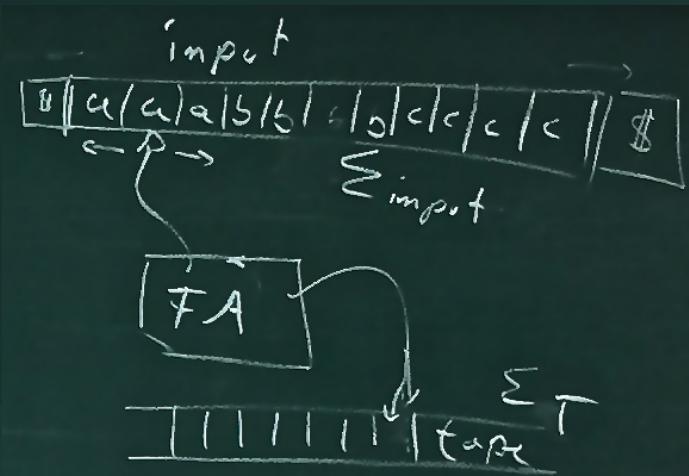
(Church-Turing)-Thesis

Everything computable can be computed by a single tape TM

Def 1-Tape TM with input tape.

$$M = (\Sigma_{\text{input} \cup \{\$\}}, \Sigma_T, Q, q_0, Q_{\text{acc}}, Q_{\text{rej}}, \Delta)$$

$$\Delta: Q \times \Sigma_{\text{input}} \cup \{\$\} \times \Sigma_T \rightarrow Q \times \{\leftarrow, \rightarrow\} \times \Sigma_T \times \{\$, \#\}$$



2. Memory, Tapes and TMs

configuration = global state of a TM M
at a time

- input string $x \in \Sigma^*$
- head pos. on input $h \in \mathbb{N}$
- all written letter word on tape $w \in \Sigma^*$
- head position on the tape
- state of F_A

2. Memory, Tapes and TMs

What happens when a configuration repeats?

C_{init}^* C_{acc} $\leftrightarrow x \in L(M)$

C_{init}



$C_1 \vdash C_2 \Leftrightarrow$ one calculation step

C_{init} := first configuration

C_{acc} accepting configuration $q \in Q_{acc}$

C_{ref} rejecting config $q \in Q_{ref}$