

## 53 Managing T1 and E1 Ports

T1 and E1 ports are supported on a variety of switching modules. In the OmniSwitch, T1 and E1 are used as standard WAN access ports, ATM cell switching ports, and circuit emulations ports. The following switching modules contain T1 or E1 ports:

- ASM-CE
- CSM-CE-T1/E1
- CSM-DS1/E1
- CSM-AB-IMA-T1/E1
- WSM-FT1/E1
- WSX-FT1/E1

Ports on these modules share a common set on of physical level attributes and a common set of software configuration commands. T1/E1 configuration options include frame format, line coding, and Facility Datalink Protocol. T1/E1 ports can store up to 24 hours of performance statistics for local and remote ports. These software commands do not configure time slots.

Hardware descriptions of the ASM-CE can be found in Chapter 7, “Switching Modules.” Hardware descriptions of the WSM-FT1/E1 can be found in Chapter 48, “Managing WAN Modules.” The CSM-CE-T1/E1 and the CSM-T1/E1 are adapter boards used in the CSM-U universal cell switching module; descriptions of these modules can be found in Chapter 41, “Cell Switching Modules.” And hardware descriptions of the WSX-FT1/E1 can be found in Chapter 3, “Omni Switch/Router Switching Modules.”

This chapter is divided into two parts. The first part provides an overview of T1/E1 digital services. The second part describes the configuration of physical T1 and E1 ports; this second part starts with the section, *The T1/E1 Menu* on page 53-3.

# T1 and E1 Overview

Carrier digital services were designed primarily to support digitized voice over long distances. Digital services are the primary method for carrying voice between two endpoints using two pairs of copper wire. Digital wide-area data networking uses the same digital services that were originally designed for digitized voice.

## Analog to Digital Conversion

To improve quality and reliability, long-distance phone networks upgraded their backbones from analog Frequency Division Multiplexing (FDM) to digital Time Division Multiplexing (TDM). In TDM, analog data is converted to digital data using a CODEC device that employs a method called Pulse Code Modulation (PCM).

In Pulse Code Modulation, the CODEC samples the analog signal 8,000 times a second and converts each sample to an 8-bit digital value. These 8,000 8-bit samples yield a total digital data rate of 64,000 BPS for one voice service. This service is also known as Digital Service Zero (DS0), which is the basis for T1 and E1 connections.

These 8,000 8-bits in time are also known as a *time slot*. A *channel* is a time slot that can carry voice or data. Using Time Division Multiplexing, 24 channels (for T1) or 32 channels (for E1) are multiplexed to create a service called Digital Service 1 (DS1). The more common name for DS1 is *T1* or *E1*.

## T1 Framing

A T1 frame consists of 24, 8-bit time slots and a 1-bit synchronization and control bit. Twelve (12) T1 frames can be grouped into a *SuperFrame (SF/D4)*, or 24 T1 frames can be grouped into an *Extended SuperFrame*. In each SuperFrame, the 6th and 12th frame may contain “robbed bit” (A, B) signalling, which means the least significant bit is robbed from each time slot in the 6th and 12th frame and used for signalling. In Extended SuperFrames, this robbed-bit signalling (A, B, C, D) occurs in the 6th, 12th, 18th, and 24th frames.

## E1 Framing

The E1 frame consists of 32, 8-bit time slots (two of these time slots are used for synchronization and multiframe signalling) for 256 bits per frame at 2.048 megabits per second. Sixteen (16) E1 frames are grouped into a multiframe. An E1 multiframe can use Channel Associated Signalling (CAS) contained in time slot 16. Timeslot 16 in multiframe 0 is used for multiframe synchronization and control. Timeslot 16 of multiframes 1 through 15 are used to carry A, B, C, and D signaling bits.

## ATM Over T1/E1

When ATM is run over a T1 or E1 port, the same framing options are used. The only difference is that the payload is comprised of cells, rather than time slots.

## The T1/E1 Menu

The commands for configuring and monitoring T1 and E1 ports are contained in the **te** submenu. This submenu displays as shown below and may be accessed (when in verbose mode) by entering **te** at a system prompt.

Command	T1/E1 Port Management Menu
<b>tes</b>	View status of a T1/E1 port configuration and statistics
<b>temod</b>	Modify a T1/E1 port configuration
<b>tecls</b>	Clear framer statistics of a T1/E1 port
<b>telts</b>	Display 24-hour period statistics of a local T1/E1 port
<b>telcs</b>	Display current 15-minute statistics of a local T1/E1 port
<b>telis</b>	Display 15-minute interval statistics of a local T1/E1 port
<b>terts</b>	Display 24-hour period statistics of a remote T1/E1 port
<b>tercs</b>	Display current 15-minute statistics of a remote T1/E1 port
<b>teris</b>	Display 15-minute interval statistics of a remote T1/E1 port

  

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The commands in this menu are described in the following sections. The first command, **tes**, displays configuration information on ports. This configuration information is configured through the **temod** command. The remaining commands, listed after the **telts** command provide a variety of interval statistics for local and remote T1 and E1 connections.

## Configuring a T1 Port

The **temod** command configures a T1 port at the physical level and is generic to all such ports regardless of the logical level service, such as circuit emulation, that controls them.

To configure a T1 port, enter the following command

```
temod <slot>/<port>
```

where **<slot>** is the slot number where the board is located, and **<port>** is the T1 port number on the board that you want to modify. For example, to modify port number 2 on the board in switch slot 5, enter

```
temod 5/2
```

A screen similar to the following displays:

### T1 Port Configuration for slot 5, port 2

1) Circuit Identifier (30 chars max)	: Alcatel T1 Circuit
2) Frame Format { ESF (2), SF (3), unframed (8) }	: ESF
3) Line Build Out { short(1), long(2) }	: short
30) Line Length in meters (0-200)	: 30
4) Line Coding { B8ZS (2) , AMI (5) }	: B8ZS
5) Facility Datalink { ANSI T1.403 (2), AT&T 54016 (4), T1.403-AT&T (6), none (8) }	: none
6) Facility Datalink Port Role { network (1), user (2) }	: network
7) Transmit Clock Source { loopTiming (1), localTiming (2) }	: localTiming
8) Loopback Mode { none (1), payload (2), line (3), inward (5) }	: inward
9) Signalling { none (1), CAS (2), CCS (3) }	: none
10) Trap Generation { enabled (1), disabled (2) }	: disabled
11) Yellow Alarm Detection { enabled (1), disabled (2) }	: enabled

Enter (option=value/save/cancel) :

### 1) Circuit Identifier

Enter a textual description of this T1 port, up to 30 characters. This text will be used in other screen displays to identify this T1 port.

### 2) Frame Format

Specify the frame format to be used on this port. The choices are Extended SuperFrame (**ESF**), SuperFrame or D4 (**SF**), or no special frame format (**unframed**). A T1 frame consists of 24 8-bit time slots and a 1-bit synchronization and control. Twelve (12) T1 frames can be grouped into a SuperFrame, and 24 T1 frames can be grouped into an Extended SuperFrame.

Normally, you should configure a T1 port as ESF (the default) since a T1 port configured as SuperFrame (SF) can produce false yellow alarms if a Layer 2 protocol like High-Level Data Link Control (HDLC) is being used. On ATM T1 ports, only option **2** (ESF) is supported since only the ESF format is compliant with the ATM Forum *DS1 Physical Layer Specification* (af-phy-0016.00). In addition, to support FDL and remote loopback activation/deactivation on the ATM UNI you *must* use the ESF format.

If you must set the port as SF, you can disable Yellow Alarm detection with the **Yellow Alarm Detection** option, which is described on page 53-7.

If you choose the **unframed** format, then the framer will not look for Channel Associated Signalling (CAS). Data is treated as a data stream. When used in a circuit emulation application, this option must be chosen when configuring an “unstructured” circuit emulation service.

### Important Note

The unframed format is only valid on ASM and CSM circuit emulation modules. You *cannot* use it on WAN modules or on ASM and CSM T1 ports without circuit emulation.

## 3) Line Build Out

Indicate whether the T1 port supports short haul or long haul interfaces. Only T1 ports equipped with Line Interface Unit (LIU) support long haul. Long haul support is necessary if this T1 port is directly connected to a Central Office (CO) and the cable length is greater than 655 feet (200 meters). If this T1 port connects locally (i.e., it is not connected to an external CSV) using less than 655 feet (200 meters) of cable, short haul is adequate.

### Note

All T1/E1 ports except those on the ASM-CE are equipped with a Line Interface Unit (LIU) chip.

An additional prompt displays for either the line length between this port and the T1 device (short haul configurations) or the attenuation of the cable attaching this port and the T1 device (long haul configurations). Each of these options is described below.

### 40) Line Length in meters

Displayed only when **short haul** is chosen as the **Line Build Out** option. Specify the distance, in meters, between this T1 port and the attached T1 device.

### 41) Attenuation

Displayed only when **long haul** is chosen as the **Line Build Out** option. Specify the attenuation of the line between this T1 port and the attached T1 device.

## 4) Line Coding

The type of physical encoding used on the connection. AMI (Alternate Mark Inversion) is more sensitive. B8ZS (Bipolar 8 Zero Substitution) should be used when possible. In most networks, B8ZS is recommended. If the port is running ATM traffic, B8ZS is *required*. In all cases, the Line Coding you select must match that provided by your service provider.

### 5) Facility Datalink

Facility Datalink (FDL) gathers performance statistics every second and stores them in the 24-hour local statistical database. It also sends local performance statistics to the remote T1 port depending on the type of FDL chosen and the “role” of the FDL (specified in the next field). In order to obtain far-end, or remote, performance statistics (viewed through the **terts**, **tercs**, and **teris** commands), you must enable an FDL protocol.

#### Note

Facility Datalink requires a T1 port and the frame type must be Extended SuperFrame.

You have the following choices:

- |                        |  |
|------------------------|--|
| <b>ANSI T1.403</b>     | The FDL exchange recommended by ANSI. The FDL method sends Performance Report Messages (PRMs) to the far-end port every second, processes received PRMs, and stores them in a 24-hour far-end statistical database.  |
| <b>AT&amp;T 54106</b>  | The operation of this FDL protocol depends on the <b>Facility DataLink Port Role</b> setting (configured in the next field). The FDL protocol will either be active ( <b>network</b> ) or passive ( <b>user</b> ) in its sending of PRMs.  |
| <b>T1.403-AT&amp;T</b> | In this combination selection, the port supports both the ANSI ( <b>ANSI T1.403</b> ) and AT&T Extended Superframe (AT&T 54106) protocols at the same time. The port processes ANSI messages as described for the <b>ANSI T1.403</b> option and responds to AT&T request messages. |
| <b>none</b>            | The port does not use Facility Datalink.   |

### 6) Facility Datalink Port Role

Indicates the role of this port in relation to the remote port. This setting only affects configurations where the Facility Datalink field is set to **AT&T 54016**. When set to **network**, far-end historical statistics are updated by periodically sending 24-hour and 1-hour performance statistics requests to the far-end port. When set to **user**, the FDL passively waits for messages from the far-end port.

### 7) Transmit Clock Source

The source of the transmit clock. Loop timing means the receive clock (recovered from receive data) is used as the transmit clock. Local timing indicates the local clock source (generated from PLLs) is used as the transmit clock.

The transmit clock source is related to the clocking mode used in circuit emulation services. In *synchronous* clocking mode, both sides of the T1 connection will use a local clock source. However, in *SRTS* and *adaptive* clocking, the T1 port receives the clock on one end (loop timing) and regenerates the clock locally (local timing) on the other end. In such a case, the T1 port receiving the clock from the network should be configured as **loop timing** and the other end of the link should be configured as **local timing**.

For more information on CSM timing, see Chapter 47, “Clocking ATM Networks.”

## 8) Loopback Mode

The loopback configuration for this port. Loopback configurations describe the relation between the device attached to a T1 port and the framing functionality within the T1 port. Framing functionality assembles T1 frames into SuperFrames and Extended SuperFrames, depending on how the port is configured. Possible values are as follows:

<b>none</b>	The port is not in a loopback state. This is the typical live network state for a T1 port.
<b>payload</b>	The received signal at this T1 port is looped out of the port after passing through the port's framing functionality. This state should only be used for debugging purposes.
<b>line</b>	The received signal at this T1 port does not go through the port's framing functionality, and is looped straight back out the port. This state should only be used for debugging purposes.
<b>inward</b>	The transmitted signal from the inward side of this port is looped back internally. The signal passes through the T1 framing functionality before looping back. This state should only be used for debugging purposes.

## 9) Signaling

The type of signaling used on this port. Only the **none** and **CAS** (Channel Associated Signaling) options are applicable to a circuit emulation service port. The **CCS** (Common Signal Channeling) option is used with external ISDN Primary Rate ports. If you select the **CAS** option, then you are enabling robbed-bit signalling.

Robbed-bit signalling can be used with SuperFrames or Extended SuperFrames. In each SuperFrame, the 6th and 12th frame may contain "robbed bit" (A, B) signalling, which means the least significant bit is robbed from each time slot in the 6th and 12th frame and used for signalling. In Extended SuperFrames, this robbed-bit signalling (A, B, C, D) occurs in the 6th, 12th, 18th, and 24th frames.

## 10) Trap Generation

Enables all of the SNMP-based traps related to T1 and E1 ports.

## 11) Yellow Alarm Detection

Specify the yellow alarm detection state for this port. A T1 port configured as SuperFrame (SF) can produce false yellow alarms if a Layer 2 protocol like High-Level Data Link Control (HDLC) is being used. Therefore, you can disable yellow alarm detection with this option. (A T1 port set to Extended SuperFrame (ESF) will not produce false yellow alarms.)

# Configuring an E1 Port

The **temod** command configures an E1 port at the physical level and is generic to all such ports regardless of the logical level service, such as circuit emulation, that controls them. You configure the circuit emulation service that controls this port through the **cemodify** command.

To configure an E1 port, enter the following command

```
temod <slot>/<port>
```

where **<slot>** is the slot number where the board is located, and **<port>** is the T1 port number on the board that you want to modify. For example, to modify port number 2 on the board in switch slot 4, you would enter

```
temod 4/2
```

A screen similar to the following displays:

### E1 Port Configuration for slot 4, port 2

1) Circuit Identifier (30 chars max)	: Alcatel E1 Circuit
2) Frame Format { E1 (4), E1-CRC (5), E1-MF (6), E1-CRC-MF (7), unframed (9) }	: E1
3) Not FAS { enabled (1), disabled (2) }	: enabled
4) Line Build Out { short(1), long(2) }	: short
40) Cable Type { 75 Ohm (1), 120 Ohm (2) }	: 75 Ohm
5) Line Coding { HDB3 (3), AMI (5) }	: HDB3
6) Transmit Clock Source { loopTiming (1), localTiming (2) }	: localTiming
7) Loopback Mode { none (1), payload (2), line (3), inward (5) }	: none
8) Signalling { none (1), CAS (2), CCS (3) }	: none
9) Trap Generation { enabled (1), disabled (2) }	: disabled

Enter (option=value/save/cancel) :

## 1) Circuit Identifier

Enter a textual description of this E1 port, up to 30 characters. This text will be used in other screen displays to identify this E1 port.

## 2) Frame Format

Specify the E1 frame format to be used on this port. The choices are as follows:

- E1** Standard E1 frame format using the framing bits in time slot 0 for framing.
- E1-CRC** E1 frame using framing bits in both time slot 0 and CRC-4 multiframe for framing.
- E1-MF** E1 frame using framing bits in both time slot 0 and time slot 16 multiframe for framing.
- E1-CRC-MF** E1 frame using framing bits in time slot 0, time slot 16 multiframe, and CRC-4 multiframe for framing.
- unframed** The framing software will not look for framing bits to determine the start of a frame or multiframe. Data is treated as a data stream. When used in a circuit emulation application, this option should be chosen when configuring an “unstructured” circuit emulation service.

### Important Note

The unframed format is only valid on ASM and CSM circuit emulation modules. You *cannot* use it on WAN modules or on ASM and CSM E1 port without circuit emulation.

## 3) Not FAS

Indicates whether you want to add an extra level of frame checking. E1 frames in time slot 0 are composed of alternating bits of FAS (Frame containing Frame Alignment Signal) and NFAS (Frame not containing Frame Alignment Signal). The **Not FAS** option tells the framer to check framing on FAS and NFAS bits. Normally, the framer checks only FAS bits, which contain the frame alignment signal pattern. If you enable **Not FAS**, then framing software will additionally also check NFAS bits, which include remote alarm indication information.

## 4) Line Build Out

The E1 port supports short haul or long haul interfaces. Only E1 ports equipped with a Line Interface Unit (LIU) chip support long haul. Long haul support is necessary if this E1 port is directly connected to a Central Office (i.e., not connected via an external CSU) and the cable length is greater than 655 feet (200 meters). If this E1 port connects locally using less than 665 feet (200 meters) of cable, then short haul is adequate.

### Note

All T1/E1 ports except those on the ASM-CE are equipped with a Line Interface Unit (LIU) chip.

An additional prompt displays requesting the resistance type used for this port connection.

### 40) Cable Type

Indicate the cable resistance type used on the short or long haul interface. The cable resistance type can be 75 ohm or 120 ohm. The resistance is set via a jumper on the E1 board; it is not configurable through software.

### 5) Line Coding

The type of physical encoding used on the connection. AMI (Alternate Mark Inversion) is more sensitive. HDB3 (High Density Bipolar 3) should be used when possible.

### 6) Transmit Clock Source

The source of the transmit clock. Loop timing means the receive clock (recovered from receive data) is used as the transmit clock. Local timing indicates the local clock source (generated from PLLs) is used as the transmit clock.

The transmit clock source is related to the clocking mode used in circuit emulation services. In synchronous clock mode, both sides of the E1 connection will use a local clock source. However, in SRTS and adaptive clocking, the E1 port receives the clock on one end (loop timing) and regenerates the clock locally (local timing) on the other end. In such a case, the E1 port receiving the clock from the network should be configured to **loop timing** and the other end of the link should be configured to **local timing**.

### 7) Loopback Mode

The loopback configuration for this port. Loopback configurations describe the relation between the device attached to an E1 port and the framing functionality within the E1 port. Framing functionality assembles E1 frames into multiframes, depending on how the port is configured. Possible values are as follows:

<b>none</b>	The port is not in a loopback state. This is the typical live network state for an E1 port.
<b>payload</b>	The received signal at this E1 port is looped out of the port after passing through the port's framing functionality. This state should only be used for debugging purposes.
<b>line</b>	The received signal at this E1 port does not go through the port's framing functionality, and is looped straight back out the port. This state should only be used for debugging purposes.
<b>inward</b>	The transmitted signal from the inwawrd side of this port is looped back internally. The signal passes through the E1 framing functionality before looping back. This state should only be used for debugging purposes.

### 8) Signalling

The type of signaling used on this port. Only the **none** and **CAS** (Channel Associated Signaling) options are applicable to a circuit emulation service port. The **CCS** (Common Signal Channeling) option is used with external ISDN ports. If you select the CAS option, then you are enabling Channel Associated Signalling, which is used with E1 multiframes. In Channel Associated Signaling, timeslot 16 in frame 0 of the multiframe is used for multiframe synchronization and control. Timeslot 16 of frames 1 through 15 are used to carry A, B, C, and D signaling bits.

### 9) Trap Generation

Enables all of the SNMP-based traps related to circuit emulation service ports.

## Viewing T1/E1 Configuration and Alarm Information

You can view all current parameters and alarms for a T1 or E1 port using the **tes** command. These parameters will be either the default parameters or parameters you modified through the **temod** command or network management software.

You have a choice of viewing parameters at the chassis or port level. You receive different displays depending upon which level you choose. The sections below describe all ways to use the **tes** command.

### Viewing Information for all T1/E1 Ports in the Switch

To view port parameters for all T1/E1 ports in a chassis, enter the following command

```
tes
```

A screen similar to following displays:

T1/E1 Chassis Status		
Slot/Port	Type	Active Alarms
=====	=====	=====
4/2	E1	NoAlarm
4/3	E1	NoAlarm
5/2	T1	NoAlarm, Loopback
5/3	T1	NoAlarm, Loopback

**Slot/Port.** The T1 or E1 slot and port for which information is supplied. The slot is listed first, followed by a slash (/), followed by the port number.

**Type.** The port type. The port will either be a T1 or E1 port.

**Active Alarms.** Alarms that have occurred on this port. Possible alarms for each port are:

<b>NoAlarm</b>	The port is free of any alarms.
<b>RcvYellow</b>	This port is receiving a yellow alarm from the far-end port. A yellow alarm occurs in SuperFrames when bit 6 of all channels has been zero for at least 425 milliseconds. The yellow alarm will not occur if a Loss of Signal alarm has already occurred. In Extended SuperFrames, an alarm occurs if the yellow alarm pattern is found.
<b>XmtYellow</b>	The port is transmitting a yellow alarm <i>to</i> the far-end port. See the above definition of <b>RcvYellow</b> for a description of a yellow alarm.
<b>RcvAIS</b>	This port is receiving Alarm Indication Signal (AIS) from the far-end port. An AIS occurs when an unframed signal with a high density of 1s (99.9% density) is received for more than 1.5 seconds.
<b>XmtAIS</b>	This port is transmitting Alarm Indication Signal (AIS) <i>to</i> the far-end port. An AIS occurs when an unframed signal with a high density of 1s (99.9% density) is received for more than 1.5 seconds.
<b>RedAlarm</b>	The port is in red alarm state. A red alarm occurs when a T1 port has been in Out-of-Frame (OOF) condition for 2.55 seconds. The red alarm condition will be removed if the OOF condition has been absent for at least 16.6 seconds.

LossOfSignal	The port has experienced a Loss of Signal (LOS), or Loss of Carrier. An LOS event occurs after 175 contiguous pulse positions with no pulses (10 absent pulses on E1 ports). An LOS failure is cleared after the switch observes a single pulse.
RcvLOMF	This port is receiving loss of multiframe (LOMF) alarms from the far-end port. When a far-end E1 port detects an out-of-multiframe condition, it transmits a frame with the alarm indication bit set (in time slot 16) back to the local E1 port. This error is similar to a yellow alarm on T1 ports.
LocalUA	This port is not available possibly because a cable is not attached.
Loopback	The port is currently in loopback mode. Loopback mode can be configured through the <b>temod</b> command or dynamically activated through Facility Data Link (ANSI T1.403 and AT&T 54106) or through loopback control codes on a T1 port.

Viewing Information for T1/E1 Ports on One Module

To view port parameters, enter the following command

```
tes <slot>
```

where **<slot>** is the slot number where the on which you want to view information resides. For example, to view configuration parameters for the board in slot 5, enter

```
tes 5
```

A screen similar to following displays:

T1/E1 Port Status for slot 5		
Port	Type	Active Alarms
=====	=====	=====
2	T1	NoAlarm, Loopback
3	T1	NoAlarm, Loopback

Explanations of the columns in this table are described in the section, *Viewing Information for all T1/E1 Ports in the Switch* on page 53-11.

## Viewing Information For a T1 Port

To view T1 port parameters, enter the following command

```
tes <slot>/<port>
```

where **<slot>** is the slot number where the board is located, and **<port>** is the T1 port number on the board on which you want to view information. For example, to view information for Port 2 on the board in slot 5, enter

```
tes 5/2
```

A screen similar to following displays for a T1 port:

### T1/E1 Port Status for slot 5, port 2

Circuit Identifier	: Alcatel T1 Circuit		
Frame Format	: ESF	Line Build Out	: 30 (SH)
Facility Datalink	: none	FDL Port Role	: network
Line Coding	: B8ZS	Signalling	: none
Transmit Clock Source	: localTiming	Trap Generation	: disabled
Status Change Time	: 0 days, 00:07:24.69		
Loopback Status	: LocalInwardLoop		
Line Status	: NoAlarm, Loopback		

#### Framer Statistics

Loss of Signal Events	: 0
Line Code Violation Events	: 431986
Out of Frame Events	: 0
Red Alarm Events	: 1
Squelch Alarm Events	: 0
Frame Bit Error Events	: 2
Alarm Indication Signal Events	: 0
Yellow Alarm Events	: 1
ESF CRC-6 Error Events	: 3

**Circuit Identifier, Frame Format, Line Build Out, Facility Datalink, FDL Port Role, Line Coding, Signalling, Transmit Clock Source, Trap Generation.** These parameters are described in the section, *Configuring a T1 Port* on page 53-4. Please refer to that section for descriptions.

**Status Change Time.** The system time when the last change in Line Status (i.e., alarm) parameter occurred.

**Loopback Status.** The type of loopback mode configured for this port through the **temod** command or activated remotely through FDL. Loopback modes are described in *Configuring a T1 Port* on page 53-4.

**Line Status.** A list of any alarms that have occurred on his port. The possible items in the list are the same as those for **Active Alarms** described in *Viewing Information for all T1/E1 Ports in the Switch* on page 53-11.

**Loss of Signal Events.** The total number of Loss of Signal (LOS) events that have been detected on this port. An LOS event occurs after 175 contiguous pulse positions with no pulses (10 absent pulses on E1 ports). An LOS failure is cleared after the switch observes a single pulse.

**Line Code Violation Events.** The occurrence of either a bipolar violation or an excessive zeros error. A bipolar violation is the occurrence of a pulse of the same polarity as the previous pulse. In B8ZS coded signals, a bipolar violation is a pulse of the same polarity as the previous without being part of the zero substitution code. An excessive zeros error is the occurrence of more than 15 contiguous zeros in an AMI-coded signal; in a B8ZS-coded signal, it is the occurrence of seven (7) or more contiguous zeros.

**Out of Frame Events.** The total number of out of frame events that have been detected on this port. An out of frame event occurs when two or more framing errors occur within a 3 microsecond period for Extended SuperFrame signals, or when two or more errors occur out of five or fewer consecutive framing bits. The signal will be back in frame when there have been fewer than two frame bit errors within a 3 microsecond period for Extended SuperFrame signals.

**Red Alarm Events.** The number of times this port has been in a red alarm state, which occurs when a T1 port has been in Out-of-Frame (OOF) condition for 2.55 seconds. The red alarm condition will be removed if the OOF condition has been absent for at least 16.6 seconds.

**Squelch Alarm Events.** The number of squelch alarm events that have been detected on this port. A squelch alarm occurs when the line signal level of the input pulse is below a threshold level. The threshold level on a T1 line is 0.5V.

**Frame Bit Error Events.** The number of framing bit error events that have been detected on this port. A frame bit error occurs when an error bit is detected during the framing process.

**Alarm Indication Signal Events.** The number of Alarm Indication Signal (AIS) events that have been detected on this port. An AIS occurs when an unframed signal with a high density of 1s (99.9% density) is received for more than 1.5 seconds.

**Yellow Alarm Events.** The total number of yellow alarm events that have occurred on this T1 port. A yellow alarm occurs in SuperFrames when bit 6 of all channels has been zero for at least 335 microseconds. The yellow alarm will not occur if a Loss of Signal alarm has already occurred. In Extended Superframes, an alarm occurs if the yellow alarm pattern is found.

### Note

A T1 port that has been configured as a SuperFrame (SF) port can produce false yellow alarms. You can disable yellow alarm detection on a T1 port with the **temod** command, which is described in *Configuring a T1 Port* on page 53-4.

**ESF CRC-6 Error Events.** The number of times a CRC-6 error has been found in an Extended SuperFrame.

## Viewing Information For an E1 Port

To view E1 port parameters, enter the following command

```
tes <slot>/<port>
```

where **<slot>** is the slot number where the board is located, and **<port>** is the E1 port number on the board for which you want to view information. For example, to view information for Port 2 on the board in slot 4, enter

```
tes 4/2
```

A screen similar to following displays for an E1 port:

### T1/E1 Port Status for slot 4, port 2

Circuit Identifier	: Alcatel E1 Circuit		
Frame Format	: E1	Line Build Out	: 120 Ohm (SH)
Line Coding	: HDB3	Signalling	: none
Transmit Clock Source	: localTiming	Trap Generation	: disabled
Status Change Time	: 0 days, 00:06:34.69		
Loopback Status	: NoLoop		
Line Status	: NoAlarm		

### Framer Statistics

Loss of Signal Events	:	1
Line Code Violation Events	:	9
Out of Frame Events	:	2
Red Alarm Events	:	1
Squelch Alarm Events	:	1
Frame Bit Error Events	:	9
Alarm Indication Signal Events	:	3
Out of Sub-multiframe Events	:	0
Out of TS16 Multiframe Events	:	0
Far End Frame Alarm Events	:	2
Far End Multiframe Alarm Events	:	0
Far End Block Error Events	:	0
CRC-4 Error Events	:	0

**Circuit Identifier, Frame Format, Line Build Out, Line Coding, Signaling, Transmit Clock Source, Trap Generation.** These parameters are described in the section, *Configuring an E1 Port* on page 53-8. Please refer to that section for descriptions.

**Status Change Time.** The system time when the last change in Line Status (i.e., alarm) parameter occurred.

**Loopback Status.** The type of loopback mode configured for this port through the **temod** command. Loopback modes are described in *Configuring an E1 Port* on page 53-8.

**Line Status.** A list of any alarms that have occurred on this port. The possible items in the list are the same as those for **Active Alarms** described in *Viewing Information for all T1/E1 Ports in the Switch* on page 53-11.

**Loss of Signal Events.** The total number of Loss of Signal (LOS) events that have been detected on this port. An LOS event occurs after the port detects more than 10 consecutive zeros.

**Line Code Violation Events.** The occurrence of either a bipolar violation or excessive zeros error. A bipolar violation is the occurrence of a pulse of the same polarity as the previous pulse. In HDB3 coded signals, a bipolar violation is a pulse of the same polarity as the previous without being part of the zero substitution code. An excessive zeros error is the occurrence of more than 15 contiguous zeros in an AMI-coded signal; in an HDB3-coded signal, it is the occurrence of seven (7) or more contiguous zeros.

**Out of Frame Events.** The total number of out of frames events that have been detected on this port. An out of frame event occurs when three consecutive frame alignment signals have been received with an error. The signal will be back in frame when frame alignment signalling is normal for three consecutive frames.

**Red Alarm Events.** The number of times this port has been in a Red alarm state. A red alarm occurs when a T1 port has been in Out-of-Frame (OOF) condition for 2.55 seconds. The red alarm condition will be removed if the OOF condition has been absent for at least 16.6 seconds.

**Squelch Alarm Events.** The number of squelch alarm events that have been detected on this port. A squelch alarm occurs when the line signal level of the input pulse is below a threshold level.

**Frame Bit Error Events.** The number of framing bit error events that have been detected on this port. A frame bit error occurs when an error bit is detected during the framing process.

**Alarm Indication Signal Events.** The number of Alarm Indication Signal (AIS) events that have been detected on this port. An AIS occurs when an unframed signal with a high density of 1s (99.9% density) is received for more than 1.5 seconds.

**Out of Sub-multiframe Events.** The number of sub-multiframe events that have been detected on this E1 port. This error occurs when four (4) consecutive CRC-4 multiframe alignment signals have been received in error or when a frame alignment error has been lost.

**Out of TS16 Multiframe Events.** The number of TS16 multiframe events that have been detected on this E1 port. This error occurs when two (2) consecutive TS16 multiframe alignment signals have been received in error, or all bits in time slot 16 are logic 0 for one TS16 multiframe, or frame alignment has been lost.

**Far End Frame Alarm Events.** The number of times the remote end has detected an out-of-frame condition. When a far end E1 port detects an out-of-frame condition, it transmits a frame with the alarm indication bit set (in time slot 0) back to the local E1 port. This error is similar to a yellow alarm on T1 ports.

**Far End Multiframe Alarm Events.** The number of times the remote end has detected an out-of-multiframe condition. When a far-end E1 port detects an out-of-multiframe condition, it transmits a frame with the alarm indication bit set (in time slot 16) back to the local E1 port. This error is similar to a yellow alarm on T1 ports.

**Far End Block Error Events.** The number of times the remote end has received a frame with a bad CRC-4. When the far end E1 port detects a CRC-4 error in the incoming frame, it transmits the frame with the E bit cleared.

**CRC-4 Error Events.** The number times a frame has been received with a bad CRC-4.

## Viewing T1/E1 Local Statistics

There are a number of commands available for viewing local T1 and E1 statistics. These commands provide statistics for the past 24 hours, the current 15-minute interval, or the past 96 15-minute intervals. The following sections describe these commands.

### Viewing Total Local Statistics

You can view statistics occurring during the past 24 hours on a single port by entering the following command

```
telts <slot>/<port>
```

where **<slot>** is the slot number where the board is located, and **<port>** is the port number on the board for which you want to view statistics. For example, to view 24-hour statistics for Port 2 on the board in slot 5, enter

```
telts 5/2
```

A screen similar to the following displays:

```

                                Local 24-hour Period Statistics for port 2 on slot 5

Circuit Identifier      : Alcatel T1 Circuit
Valid Intervals        : 1 of 96      Elapsed Time          : 421 of 900

  ES   SES  BES   UAS  SEFS   LES   CSS   PCV   LCV
  ----
    3    1    1    0    1   313    0    2   313

```

**Circuit Identifier.** The textual description of this T1 or E1 port as configured through the **temod** command.

**Valid Intervals.** Indicates the number of 15-minute intervals for which valid statistics were gathered during the previous 24 hours. Statistics may be gathered for up to 96 15-minute intervals during a 24 hour period.

**Elapsed Time.** The number of seconds that have elapsed during this 15-minute interval of gathering statistics. This time will be reset to zero when a 15-minute session of statistics gathering is complete (and stored) and the next 15-minute interval begins.

**ES.** Errored Seconds. For T1-ESF and E1-CRC conditions, this is a second with one or more Path Code Violations, one or more out-of-frame defects, one or more controlled slip errors, or an AIS error.

**SES.** Severely Errored Seconds. For T1-ESF frames, this is a second with 320 or more Path Code Violation errors, one or more out-of-frame defects, or an AIS error. For E1-CRC conditions, this is a second with 832 or more Path Code Violation errors, or one or more out-of-frame defects. For E1-noCRC signals, this is a second with 2048 or more Line Code Violation errors. For D4/(SF) frames, this is a second with framing errors, an out-of-frame error, or a second with 1544 or more line code violation errors.

**BES.** Bursty Errored Seconds. The number of seconds with fewer than 320 but more than one (1) Path Code Violation error (see below for definition), no Severely Errored Frame errors, and no AIS errors.

**UAS.** Unavailable Seconds. The number of seconds this port was unavailable for transmitting or receiving data. In general, a port is unavailable after 10 consecutive Severely Errored Seconds or after a failure on the interface occurs.

**SEFS.** Severe Errored Framing Second. A second with one or more out-of-frame errors or an AIS error.

**LES.** Line Errored Seconds. The number of seconds during which one or more Line Code Violation errors have occurred (see also the definition of Line Code Violation below).

**CSS.** Controlled Slip Seconds. A one-second interval with one or more controlled slip errors. Controlled slip errors are the replication or deletion of the payload bits on a frame. Such an error may occur when there is a difference between the timing of a synchronous receiving terminal and the received signal.

**PCV.** Path Code Violations. A frame synchronization bit error in EF/D4 and E1-noCRC frames, or a CRC or frame synchronization error in the T1-ESF (Extended Super Frame) and E1-CRC frames.

**LCV.** Line Code Violations. The occurrence of either a bipolar violation or excessive zeros error. A bipolar violation is the occurrence of a pulse of the same polarity as the previous pulse. In B8ZS and HDB3 coded signals, a bipolar violation is a pulse of the same polarity as the previous without being part of the zero substitution code. An excessive zeros error is the occurrence of more than 15 contiguous zeros in an AMI-coded signal; in a B8ZS-coded signal, it is the occurrence of seven (7) or more contiguous zeros.

Viewing Current Local Statistics

You can view statistics for the current 15-minute interval on a single port by entering the following command

```
telcs <slot>/<port>
```

where **<slot>** is the slot number where the board is located, and **<port>** is the port number on the board on which you want to view statistics. For example, to view 15-minute interval statistics for Port 2 on the board in slot 5, enter

```
telcs 5/2
```

A screen similar to the following displays:

Local Current 15-minute Measurement for port 2 on slot 5									
Circuit Identifier		: Alcatel T1 Circuit							
Valid Intervals		: 1 of 96		Elapsed Time		: 431 of 900			
ES	SES	BES	UAS	SEFS	LES	CSS	PCV	LCV	
=====	=====	=====	=====	=====	=====	=====	=====	=====	
0	0	0	0	0	0	0	0	0	

Definitions of the fields and columns in this display are the same as those used for the **telts** command. See *Viewing Total Local Statistics* on page 53-17 for an explanation of these statistics.

## Viewing Local Historical Statistics

The **telis** command allows you to display historical statistics for the past 96 15-minute intervals. Enter the following command

**telis <slot>/<port>**

where **<slot>** is the slot number where the board is located, and **<port>** is the port number on the board on which you want to view statistics. For example, to view historical 15-minute interval statistics for Port 2 on the board in slot 5, enter

**telis 5/2**

A screen similar to the following displays:

### Local 15-minute Interval Statistics for port 2 on slot 5

Circuit Identifier		: Alcatel T1 Circuit								
Valid Intervals		: 5 of 96		Elapsed Time			: 440 of 900			
Intv#	ES	SES	BES	UAS	SEFS	LES	CSS	PCV	LCV	
1	0	0	0	0	0	0	0	0	0	
2	0	0	0	0	0	0	0	0	0	
3	0	0	0	0	0	0	0	0	0	
4	0	0	0	0	0	0	0	0	0	
5	0	0	0	0	0	0	0	0	0	

Definitions of the fields and columns in this display are the same as those used for the **telts** command. See *Viewing Total Local Statistics* on page 53-17 for an explanation of these statistics.

## Viewing T1 Remote Statistics

To receive and monitor remote statistics on T1 ports you must enable the Facility Datalink (FDL) protocol through the **temod** command. These statistics will not be available if you do not enable FDL.

**Note**

Because there is no FDL standard for E1 configurations, remote statistics are not supported on E1 ports.

## Viewing Total Remote Statistics

You can view remote statistics occurring during the past 24 hours on a single port by entering the following command

**ters <slot>/<port>**

where **<slot>** is the slot number where the board is located, and **<port>** is the port number on the board on which you want to view statistics. For example, to view remote 24-hour statistics for Port 2 on the board in slot 5, enter

**ters 5/2**

A screen similar to the following displays:

Remote 24-hour Period Statistics for port 2 on slot 5									
Circuit Identifier		: Alcatel T1 Circuit							
Valid Intervals		: 1 of 96		Elapsed Time		: 1 of 900			
ES	SES	BES	UAS	DM	SEFS	LES	CSS	PCV	LOFC
=====	=====	=====	=====	=====	=====	=====	=====	=====	=====
0	0	0	0	0	0	0	0	0	0

Most of the definitions of the fields and columns in this display are the same as those used for the **ters** command. See *Viewing Total Local Statistics* on page 53-17 for an explanation of these statistics. The remaining statistics are described below.

**LOFC.** Loss of Frame Count. A loss of frame count is the accumulation of the number of times a “Loss of Frame” is declared.

## Viewing Current Remote Statistics

You can view remote statistics for the current 15-minute interval on a single port by entering the following command

**tercs <slot>/<port>**

where **<slot>** is the slot number where the board is located, and **<port>** is the port number on the board on which you want to view statistics. For example, to view remote 15-minute interval statistics for Port 2 on the board in slot 5, enter

**tercs 5/2**

A screen similar to the following displays:

```

Remote Current 15-minute Measurement for port 2 on slot 5

Circuit Identifier      : Alcatel T1 Circuit
Valid Intervals        : 1 of 96      Elapsed Time           : 1 of 900

  ES   SES   BES   UAS   DM   SEFS   LES   CSS   PCV   LOFC
=====
    0     0     0     0     0     0     0     0     0     0

```

Definitions of the fields and columns in this display are the same as those used for the **telts** command. See *Viewing Total Local Statistics* on page 53-17 for an explanation of these statistics.

## Viewing Remote Historical Statistics

The **teris** command allows you to display remote historical statistics for the past 96 15-minute intervals. Enter the following command

**teris <slot>/<port>**

where **<slot>** is the slot number where the board is located, and **<port>** is the port number on the board on which you want to view statistics. For example, to view remote historical 15-minute interval statistics for Port 2 on the board in slot 5, enter

**teris 5/2**

A screen similar to the following displays:

```

Remote 15-minute Interval Statistics for port 2 on slot 5

Circuit Identifier      : Alcatel T1 Circuit
Valid Intervals        : 5 of 96      Elapsed Time           : 25 of 900

Intv#   ES   SES   BES   UAS   DM   SEFS   LES   CSS   PCV   LOFC
=====
    1     0     0     0     0     0     0     0     0     0     0
    2     0     0     0     0     0     0     0     0     0     0
    3     0     0     0     0     0     0     0     0     0     0
    4     0     0     0     0     0     0     0     0     0     0
    5     0     0     0     0     0     0     0     0     0     0

```

Definitions of the fields and columns in this display are the same as those used for the **telts** command. See *Viewing Total Local Statistics* on page 53-17 for an explanation of these statistics.

### Clearing the Framer Statistics for a T1/E1 Port

The **tecls** command enables you to clear the accumulated physical-layer (Framer) statistics for a T1 or E1 port. To clear statistics, enter

**tecls <slot>/<port>**

where **<slot>** is the slot number where the board is located, and **<port>** is the port number on the board on which you want to clear statistics. For example, to statistics for Port 2 on the board in slot 5, enter

**tecls 5/2**

Once the statistics have been cleared, the following message will be displayed:

**Statistics of port 5/2 have been cleared.**